Comparative study of control strategies for oscillation damping in DC microgrids

Isaías Valente de Bessa∗ Renan Landau Paiva de Medeiros∗
Iury Valente de Bessa∗ João Edgar Chaves Filho∗
Florindo A.C. Ayres Junior∗

Abstract: Nowadays, the use of converters is attracting increasing attention due to diversification of electric power sources. In particular, DC-DC converters are used to ensure the stability in the DC supply responsible for feeding a DC microgrid with constant voltage. Thus, classical and modern controls methodologies are used to ensure the stability of the feed system when connected to a constant power load (CPL). This paper compares control methodologies for pole allocation using Diophantine equation, root locus, linear-quadratic regulator and the Lyapunov equation by using performance indices as integral of square error (ISE), integral of square weighted by time (ISET) and integral of square of control (ISSC).

Keywords: DC-DC power converters; DC microgrid; CPL; controls methodologies; performance index.

1. INTRODUCTION

The demand for new energy sources results in the increasing use of converters in power systems whether by the need to convert the AC source from the grid into a DC level for the power supply of electrical devices or the conversion of DC voltage into AC voltage for the return to the electric grid. However, between steps AC-DC and DC-AC, the treatment of this voltage are done by DC-DC converters capable of stabilizing the voltage from the AC-DC converter (Elsayed et al., 2015).

Due to the demand for DC-DC converters, its modeling has been extensively investigated in the literature, (Erickson and Maksimović, 2004), as well as control strategies for voltage regulation. In particular, in (Poodeh et al., 2007) is proposed a control strategy for buck converter using Fuzzy logic. Modern control techniques have also been used for regulation of buck converters, as in (Maccari et al., 2016) that uses LQR optimal control, where the buck is used in the auxiliary power system of an electric vehicle.

The modeling and control of DC-DC converters allow the construction of DC microgrids. Microgrids are composed of feeders that must maintain a constant voltage of DC bus to which are connected DC loads as in (Marcillo et al., 2019; Singh et al., 2017). The behavior of the constant power loads (CPL) in the connection with the DC bus is studied in (Mosskull, 2018), where it is observed oscillations in the feeder due to the constant consumption of power of the CPL that causes a variation of current and voltage for the maintenance of the constant power. These variations result in the occurrence of a negative resistance in which its effects are observed in the form of oscillations in the DC bus (Marcillo et al., 2019; Singh et al., 2017).

Ensuring the stability of the microgrid is fundamental to the proper functioning of the system and in (Riccobono and Santi, 2014) it is shown criteria that guarantee the stability of the system among which stands out the criterion of Middlebrook that ensure the DC microgrid stability if the output feeder impedance is less than the input impedance of CPL. The mitigation of CPL oscillations is already investigated in (Mosskull, 2018) that shows the relationship between good damping and small power variations, and in (Marcillo et al., 2019), that uses robust control strategies to reduce the oscillation effects.

This paper proposes a DC microgrid model where the feeder is represented by a boost converter while the CPL is designed through a buck converter. Through the connection between the two systems, it is sought to investigate control strategies for the regulation of the feeder capable of mitigating the oscillations arising from the connection to the CPL. Among the proposed structures are the output feedback structures using PID controllers designed by pole placement metrics using the Diophantine equation (DE) and graphical use of the root locus (RL) for the determination of the region of stability and performance of the system to be controlled. State feedback structures are also considered in the analysis, where the determination of controllers gain is performed using the linear-quadratic regulator (LQR) and the solution of the Lyapunov equation. Finally, an analysis of the performance index of each proposed method was performed, observing the controllers ability to mitigate the oscillations from the CPL.

The remaining of this paper is divided as follows: In Section 2 the modeling of the converters constituting the microgrid is discussed. In Section 3 a revision of control strategies to be used to stabilize the feeder in connection with the CPL is carried out. Section 4 shows the experimental procedures and the test performed to verify the performance of the feeder controllers. In Section 5 the controllers performance analysis is evaluated through performance index graphs. Finally, in Section 6 the final considerations are made about the proposal of mitigation of oscillations in the feeder, observing the results analyzed in Section 5.
2. MODELING OF DC MICROGRIDS WITH CPL

2.1 Modeling of the boost converter operating as a feeder

The boost converter showed in Fig. 1a is responsible for stepping up an input voltage level \( V_1 \) to an output level \( V_0 \), according to the relation expressed by (1), for a duty cycle \( D_1 \) (Erickson and Maksimović, 2004).

\[
V_0 = \frac{V_1}{1 - D_1}
\]  

(1)

![Diagram of boost converter](image)

**Fig. 1.** (a) Boost converter (b) Buck converter.

The inductor \( L_1 \) is determined to ensure continuous conduction operation mode \((I_{L1} > 0)\) and for a signal PWM of frequency, \( f_1 \) is obtained the variation of current \( \Delta I_{L1} \) in the inductor and the voltage variation \( \Delta V_{o1} \) in the capacitor. These relations are described as follows (Erickson and Maksimović, 2004).

\[
\begin{align*}
\Delta I_{L1} &= \frac{D_1 V_1}{f_1} L_1 D_1 \\
\Delta V_{o1} &= \frac{V_1}{R_1 C_1} \Delta I_{L1} \\
V_{o1} &= \int \frac{V_1}{R_1 C_1} \Delta I_{L1}
\end{align*}
\]  

(2)

The set of equations shown in (1) and (2) are derived from the analysis of the steady-state boost converter. The dynamic behavior is obtained through the circuit analysis of Fig. 1a in two moments: when the PWM signal is high level and low level, shown respectively in (3) and (4), as described in (Erickson and Maksimović, 2004).

\[
\begin{align*}
I_{L1} &= \frac{V_1}{L_1} \\
V_{C1} &= -\frac{V_1}{R_1 C_1}
\end{align*}
\]  

(3)

\[
\begin{align*}
\dot{I}_{L1} &= \frac{V_1}{L_1} \\
\dot{V}_{C1} &= \frac{I_{L1}}{C_1} - \frac{V_1}{R_1 C_1}
\end{align*}
\]  

(4)

To obtain a representative model of the system in the two instants of operation, it is used the average space model (Erickson and Maksimović, 2004) shown in (5) where the output information is the voltage \( V_{o1} \) on resistor \( R_1 \).

\[
\begin{bmatrix}
\dot{I}_{L1} \\
\dot{V}_{C1}
\end{bmatrix} = \begin{bmatrix}
0 & -\frac{1}{C_1} \\
\frac{1}{C_1} & -\frac{L_1}{C_1} - \frac{1}{R_1 C_1}
\end{bmatrix} \begin{bmatrix}
I_{L1} \\
V_{C1}
\end{bmatrix} + \begin{bmatrix}
\frac{1}{L_1} \\
0
\end{bmatrix} V_{o1}
\]

(5)

For the feeder output regulation the value of the source \( V_1 \) is constant, so the control of the output signal is performed from the duty cycle \( D_1 \) of the circuit, consisting of a fixed part \( D_1^o \) associated to the operating point of the circuit and another variable part \( \Delta D_1 \), as shown in (6).

\[
D_1(t) = D_1^o + \Delta D_1
\]  

(6)

Thus, by linearizing the system around the operating point \((D_1^o, I_{L1}, V_{C1})\), the state-space model presented in (7) is obtained.

\[
\begin{bmatrix}
\Delta I_{L1} \\
\Delta V_{C1}
\end{bmatrix} = \begin{bmatrix}
0 & -\frac{1}{C_1} \\
\frac{1}{C_1} & -\frac{L_1}{C_1} - \frac{1}{R_1 C_1}
\end{bmatrix} \begin{bmatrix}
\Delta I_{L1} \\
\Delta V_{C1}
\end{bmatrix} + \begin{bmatrix}
\frac{V_1}{C_1} \\
\frac{1}{L_1} \frac{V_1}{C_1}
\end{bmatrix} \Delta D_1
\]

(7)

where \( V_{o1}^o = \int \frac{V_1}{R_1 C_1} \Delta I_{L1} \) and \( I_{L1}^o = \frac{V_1}{R_1(1-D_1^o)} \) are respectively the voltage in the capacitor and the current in the inductor at the point of operation with duty cycle \( D_1^o \) described as follows. Finally, based on (7), the following transfer function model can be obtained for the boost converter

\[
G(s) = \frac{\Delta V_{o1}(s)}{\Delta D_1(s)} = \frac{-\frac{V_1}{C_1} s + \frac{V_1}{R_1 C_1}}{s^2 + \frac{1}{R_1 C_1} s + \frac{1}{L_1 C_1} + \frac{C_2}{(1-D_1^o s)}}
\]

(8)

2.2 Modeling of the buck converter operating as CPL

The buck converter is shown in Fig. 1b is responsible for step down a voltage level \( V_2 \) at the input to a voltage level \( V_{o2} \) at the output through the ratio shown in (9), where \( D_2 \) represents the duty cycle of the circuit operation (Erickson and Maksimović, 2004).

\[
V_{o2} = D_2 V_2
\]  

(9)

The inductor \( L_2 \) must be such that its value guarantees the operation of the circuit in continuous conduction \((I_{L2} > 0)\) and for a signal PWM of frequency \( f_2 \) is can determine the variation of current \( \Delta I_{L2} \) in the inductor and the voltage variation \( \Delta V_{o2} \) in the output of the circuit. These relationships are shown in (10) and shown in more detail in (Erickson and Maksimović, 2004).

\[
\begin{align*}
L_2 &= \frac{R_2 (1-D_2)}{f_2} \\
\Delta I_{L2} &= \frac{2 f_2}{D_2 (1-D_2) V_2} \\
\Delta V_{o2} &= \frac{L_2 f_2}{8 f_2^2 R_2 C_2} \frac{1}{D_2}
\end{align*}
\]  

(10)

For this paper the buck is treated as CPL, so the output power of the converter is given by (11) using as the basis for power \( D_2 = 1 \).

\[
P_{o2} = D_2^2
\]  

(11)
By performing the same procedures to obtain an average space-state and linearization around the operating point \((D^2_o, I^2_o, V^2_o)\), the linearized model in (12) is given.

\[
\begin{bmatrix}
\Delta I_{o2} \\
\Delta V_{C_2}
\end{bmatrix} = \begin{bmatrix}
0 & -\frac{1}{C_2} \\
1 & \frac{1}{C_2} \\
\end{bmatrix} \begin{bmatrix}
\Delta I_{o2} \\
\Delta V_{C_2}
\end{bmatrix} + \begin{bmatrix}
\frac{V_{i2}}{L_2} \\
0
\end{bmatrix} [\Delta D^2_o]
\]

(12)

With the state space linearized system in (12), the transfer function of the buck converter was determined for variations around the operating point in (13).

\[
H(s) = \frac{\Delta P_{o2}(s)}{\Delta D^2_o(s)} = \frac{2D^2_o L_2 C_2}{s^2 + \frac{R_o C_2}{L_2} s + \frac{1}{L_2 C_2}}
\]

(13)

3. CONTROL STRATEGY FOR DC MICROGRIDS WITH CPL.

For the implementation of the microgrid, the voltage regulation from the feeder is fundamental. In this way, the investigation of control strategies becomes necessary. These include classical methods such as the DE and the use of the RL graphical tool in the use of output feedback structures shown in Fig. 2a, show in (Chen, 1999). The state feedback structure, shown in Fig. 2b, is also used for the implementation of the LQR control, (D’Azzo and Houpius, 1988) or through the solution of the LE, (Chen, 1999).

![Fig. 2. (a) System with output feedback structure (b) System with state feedback structure.](image)

3.1 The Diophantine equation

Consider the output feedback structure system shown in Fig. 2a. The open-loop system transfer function \(G(s) = \frac{B(s)}{A(s)}\) is calculated directly through (14).

\[
G(s) = C(sI - A)^{-1} B
\]

(14)

When performing the system output feedback, it will have a characteristic equation \(\Delta_c(s)\), shown in (15), to represent its dynamic behavior, dependent on the designed \(C(s) = \frac{N(s)}{M(s)}\) controller.

\[
\Delta_c(s) = 1 + C(s)G(s)
\]

(15)

Give a polynomial \(\Delta_c(s)\) desired for the closed-loop system. Comparing this polynomial to the characteristic polynomial presented in (15), has the Diophantine equation, presented in (16).

\[
M(s)A(s) + N(s)B(s) = \Delta_c(s)
\]

(16)

The solution of the polynomial equation proposed in (16), when possible, allows the determination of the parameters of the controller \(C(s)\), by determining the coefficients of polynomials \(M(s)\) and \(N(s)\) (Chen, 1999).

3.2 Region of system performance through root locus

Another way to ensure the desired performance of a closed-loop system given the transfer function \(G(s)\) is through the use of the root locus tool, which given a variable parameter in controller \(C(s)\), shows the behavior of the poles of the closed-loop system of the desired polynomial \(\Delta_c(s)\), (D’Azzo and Houpius, 1988).

![Fig. 3. Desired performance region on root locus](image)

The desired polynomial is formed by design specifications such as settling time \(t_{ss}\) and damping coefficient \(\zeta\).

The settling time is related to the time constant \(\tau \simeq \frac{t_{ss}}{\zeta}\) of the system to be analyzed. Where the damping \(\sigma\) of the system is related to the time constant as indicated in (17).

\[
\sigma = \frac{1}{\tau} \quad \zeta = \cos \theta
\]

(17)

The damping coefficient \(\zeta\) is related to the angle \(\theta\) that the line segment from the origin to the dominant pole realizes with the real axis, as indicated in (17). Thus, the desired performance region in the plane of the complex is determined, as indicated in Fig. 3, where the desired performance region is hatched.

3.3 The linear-quadratic regulator

The LQR is an optimal control methodology capable of allowing the designer choosing between minimizing energy or minimizing time, by developing the Quadratic Performance Index shown in (18), (D’Azzo and Houpius, 1988).

\[
I = \int_t^\infty (x^T Q x + u^T R u) dt
\]

(18)

For the use of the LQR methodology, the state feedback structure is used, as shown in Fig. 2b. The solution of the optimal control problem is to determine the gain value \(K\) for the state feedback, utilizing the solution of the Riccati equation, see (D’Azzo and Houpius, 1988), shown in (19) resulting from the development of (18).

\[
PA + A^T P - PBR^{-1} B^T P = -Q
\]

(19)
where $Q$ is symmetric semi-definite positive matrix and $R$ is a defined positive determined by the designer. The solution of (19) results in the definition of the matrix $P$. The values of the gains of the matrix $K$ are given by (20).

$$K = R^{-1}B^TP$$  \hfill (20)

The weighting of $Q$ and $R$ are related to the project’s need to prioritize better performance or less control effort.

### 3.4 Lyapunov equation

Considering the system presented in the open-loop controlled Fig. 2b, the gain value $K$ is determined for the system to satisfy specifications by the solution of the Lyapunov equation shown in (21) (Chen, 1999).

$$AT - TF = BK_T$$  \hfill (21)

Where $F$ is a matrix whose eigenvalues are equal to the roots of the desired polynomial $\Delta_\omega(s)$. The matrix $K_T$ is arbitrarily chosen such that $FK_T$ is observable and the matrix $T$ is the solution of the Lyapunov equation. With the matrix $T$, the state feedback gain $K$ is determined through (22).

$$K = K_T T^{-1}$$  \hfill (22)

### 4. METHODOLOGICAL PROCEDURES

This paper proposes to evaluate the performance of the controllers proposed for the feeder for the voltage regulation in the DC bus of the microgrid proposed in Fig. 4. The design of the parameters of the microgrid as well as the gains of the controllers is discussed in this section besides the test performed for the validation and analysis of the performance of the controllers.

#### 4.1 DC microgrid design

The feeder is represented for a boost converter, and its project is based on a switching frequency $f_1 = 2$ KHz, limited by $L_1 = 1$ mH inductor and an input voltage $V_{i1} = 12$ V. In this way, the resistance parameters $R_1$, capacitor $C_1$, duty cycle $D_1$ and output voltage $V_{o1}$ were determined for an input current variation of $\Delta I_{i1} \leq 2$ A based on the set of equations shown in (2). On the other hand, the design of the CPL, represented for a buck converter, is based on a switching frequency $f_2 = 4$ KHz, limited by $L_2 = 1$ mH inductor and an input voltage $V_{i2} = 18$ V. In this way, the resistance parameters $R_2$, capacitor $C_2$, Duty Cycle $D_2$ and output voltage $V_{o2}$ are determined for an input current variation of $\Delta I_{i2} \leq 1.5$ A based on the set of equations shown in (10). Table 1 shows the computed parameters.

The rated power of the CPL is determined for $D_2 = 1$, where for an input voltage of $V_{i2} = 18$ V and a load of $R_2 = 5$Ω. Thus, the rated power of the CPL is given by $P_n = V_{i2}^2 / R_2 = 64.8$ W.

<table>
<thead>
<tr>
<th>Table 1. Microgrid parameters</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Boost feeder</strong></td>
</tr>
<tr>
<td>Parameter</td>
</tr>
<tr>
<td>Input voltage</td>
</tr>
<tr>
<td>Duty cycle</td>
</tr>
<tr>
<td>Switching frequency</td>
</tr>
<tr>
<td>Load resistance</td>
</tr>
<tr>
<td>Inductor</td>
</tr>
<tr>
<td>Capacitor</td>
</tr>
<tr>
<td>Output voltage</td>
</tr>
<tr>
<td><strong>Buck CPL</strong></td>
</tr>
<tr>
<td>Parameter</td>
</tr>
<tr>
<td>Input voltage</td>
</tr>
<tr>
<td>Duty cycle</td>
</tr>
<tr>
<td>Switching frequency</td>
</tr>
<tr>
<td>Load resistance</td>
</tr>
<tr>
<td>Inductor</td>
</tr>
<tr>
<td>Capacitor</td>
</tr>
<tr>
<td>Output power</td>
</tr>
</tbody>
</table>

The CPL switching frequency is twice as fast as the feeder. This way, the feeder becomes sensitive to instantaneous CPL power variations.

#### 4.2 Controller design

After the determination of the parameters of the converters for the feeder and the CPL, the controller design is carried out. The controller structures used are shown in Fig. 5.

For the feeder and CPL controllers are designed to meet a settling time $t_{ss}$, an overshoot $ovs$ and a system with steady-state error $e_{ss}$ at step with specifications shown in (23) and (24).

$$t_{ss} < 0.02s, \quad ov < 10\%, \quad e_{ss} = 0$$  \hfill (23)  

$$ov = e^{-\frac{\zeta_n}{\sqrt{1-\zeta_n^2}}, \quad t_{ss} = \frac{4.6}{\zeta_n}}$$  \hfill (24)

For the proposed controller structure for the feeder, two auxiliary poles, $p_1$ and $p_2$, are used to ensure that the Diophantine equation is possible and determinable while for the proposed CPL structure an auxiliary pole $p_3$ is used. They are allocated so as not to interfere with the
desired behavior of the systems. The relationships used are shown in (25).

\[ p_1 = 2\zeta \omega_n, \quad p_2 = 2.5\zeta \omega_n, \quad p_3 = 3\zeta \omega_n \]  

(25)

Fig. 5. Structure proposed for the design of the boost converter controller (a) with output feedback (b) with state feedback and (c) the proposed structure for the buck converter controller as CPL.

With the information obtained from the (24) and (25) relations, the desired polynomials for the feeder, \(\Delta_d(s)\) and CPL, \(\Delta_{dc}(s)\) are constructed as (26). The gains of the CPL controllers were calculated such that \(K_p = -0.1679\), \(K_i = 127.659\) and \(K_d = 0.0015\).

\[
\begin{align*}
\Delta_d(s) &= (s^2 + 2\zeta \omega_n s + \omega_n^2)(s + p_1)(s + p_2) \\
\Delta_{dc}(s) &= (s^2 + 2\zeta \omega_n s + \omega_n^2)(s + p_3)
\end{align*}
\]  

(26)

The feeder controller is also designed using the root locus (RL). With the expected performance of the system presented in the and with the help of the rlttool() tool of MATLAB, the performance region is determined to place the poles, as indicated in Fig. 3.

The feeder controller is also designed using the Linear-Quadratic Regulator (LQR) method using the structure shown in Fig. 5a. For the problem solving of the Riccati equation the matrices, \(Q\) and \(R\) are chosen as indicated in (27). Where the weighting of a value elevated to \(R\) causes a penalty in the control action. The value of the integral gain is determined from the integrated area of the system, to guarantee zero error to the steady-state step. The gain vector \(K\) was calculated using the MATLAB lqr() command.

\[
Q = \begin{bmatrix}
1 & 0 \\
0 & 10^{-6}
\end{bmatrix}, \quad R = 100
\]  

(27)

Finally, the controller gains are projected using the Lyapunov equation (LE), whereas in the LQR structure, shown in Fig. 5b is used with an integrator at the input to ensure zero error to the step. The \(F\) matrix, shown in (28), is constructed by determining the desired polynomial with the system specifications presented in (23). The MATLAB lyap() command is used for the determination of the gain vector \(K\). The controller gains are summarized in Table 2.

\[
F = \begin{bmatrix}
\frac{\zeta \omega_n}{\omega_n \sqrt{1 - \zeta^2}} \\
-\omega_n \sqrt{1 - \zeta^2} \\
\zeta \omega_n
\end{bmatrix}
\]  

(28)

### Table 2. Feeder controller gains

<table>
<thead>
<tr>
<th>Gain</th>
<th>DE</th>
<th>RL</th>
<th>LQR</th>
<th>LE</th>
</tr>
</thead>
<tbody>
<tr>
<td>(K_p)</td>
<td>-6.421</td>
<td>25.5085</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>(K_i)</td>
<td>7.340.4</td>
<td>19.417.0</td>
<td>5</td>
<td>5</td>
</tr>
<tr>
<td>(K_d)</td>
<td>0.0963</td>
<td>0.3173</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>(N)</td>
<td>1.640.4</td>
<td>3.174.0</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>(K)</td>
<td>-</td>
<td>-</td>
<td>0.0392 0.0066</td>
<td>0.0232 0.0017</td>
</tr>
</tbody>
</table>

### 4.3 Description of the test environment

Considering the controllers designed in the previous section, the microgrid simulation is performed in Simulink/MATLAB. The system is tested for small variations in CPL power for the performance analysis of the feeder controllers.

The proposed microgrid is shown in Fig. 4. The simulation starts at time \(t = 0.0\) s. At \(t = 1.0\) s the feeder runs around the designed operating point and at \(t = 1.5\) s the feeder’s loop is closed for the controller’s actuation. Then, the CPL is connected to the system at \(t = 3.0\) s and achieves the designed operation point at \(t = 4.0\) s. The CPL controller is connected to the system at \(t = 4.5\) s.

With the system operating around the chosen operating points, the CPL power variation is done. At \(t = 5.0\) s and \(t = 5.5\) s there are step variations of \(0.1\) pu in the reference, and the system returns to the original reference value at \(t = 6.0\) s.

At \(t = 6.5\) s and \(t = 7.0\) s, there are other step variations \(-0.1\) pu in the power reference for the instants \(t = 7.5\) s and \(t = 8.0\) s to return in the reference with step variations of \(0.1\) pu. The proposed system analyzes are performed from time \(t = 5\) s, when the controllers are already working and the CPL is already connected to the DC bus.

### 5. RESULTS ANALYSIS

The microgrid showed in Fig. 4 is tested for small CPL power reference variations. It is seen if the controllers proposed for the feeder can keep the system stable. Also, the performance of each controller is verified through the performance index (PI). Among the indices to be contemplated in this work, integral of square error (ISE) is used to verify the system with smaller error accumulation, shown in Fig. 7a, to maintain the regulated system, also is contemplated the integral of square error weighted by time (ISET), Fig. 7a, to ponder the speed with which the controllers have been able to stabilize the microgrid and finally is verified the integral of the square of the signal of control (ISSC), Fig. 7a, to observe the system energetically better.

#### 5.1 Performance of controllers designed for the feeder

With the system at the point of operation, the CPL power reference variation is implemented. The result in time is shown in Fig. 6, wherein Fig. 6a is shown the voltage at which the feeder feeds the DC bus to the power reference variation shown in Fig. 6b.

It is observed by the voltage in the DC bus, Fig. 6a, that the controllers designed for the feeder can keep the system stable even in the occurrence of power variations in the CPL, where the LE methodology showed fewer oscillations about the others metrics.

The performance of controllers is analyzed in terms of IP. In Fig. 7a the ISE for each proposed methodology is
Fig. 6. (a) Voltage at the output of the feeder for (b) power variation in the CPL and performance index for design controller in the feeder.

It is observed that the LE methodology presents the lowest ISE among the four methodologies, indicating better performance in the mitigation of the CPL.

Fig. 7. (a) integral of square error (ISE) (b) integral of square error weighted by time (ISET) (c) integral of square signal of control (ISSC).

Fig. 7b shows ISET the feeder controllers by considering variations on the load power demand of CPL. The LE methodology is shown with lower ISET among the four methodologies, indicating less time to mitigate the oscillations from the CPL.

Fig. 7c describes the ISSC for the compared control methodologies. In the ISSC the LE methodology presented a higher index about the other methodologies used, indicating that to guarantee the best performance in the mitigation of oscillations, a greater control effort of the system is required. The DE, RL and LQR methodologies presented similar behavior in the three indices, where it is observed that for the increase of the power variation in the CPL the DE methodology has the greater loss of performance and increase in the time of mitigation of the oscillations. The greatest control effort of the controller by LE is related to the duty cycle variation around the operating point. In this case, the LE control design presents a greater duty cycle variation compared to the other methodologies used.

6. CONCLUSION

This paper investigates the problem of oscillation damping in DC microgrids. In particular, four control methodologies, namely, DE, RL, LQR, and LE, are used to ensure the stability and oscillation damping in DC microgrids with CPL. Thus, a comparative analysis between that control methodologies is performed. For this purpose, ISE, ISET, and ISSC are evaluated such that the dynamic behavior of microgrid is analyzed to three aspects: damping performance, time spent to mitigate the oscillations, and control effort.

Therefore, it is observed that the LE methodology presents the best performance in the mitigation of the oscillations from the CPL, being able to suppress the oscillations in the shortest time about the other methodologies, as can be seen in the presented ISE and ISET charts. However, it results in a greater effort about other methodologies, as observed in the presented ISSC chart. The other methodologies presented similar performance for mitigation of oscillations, with the observation of the DE that loses performance for excessive variations of power in the CPL.

REFERENCES


